



SSC8219GN4

P-Channel Enhancement Mode MOSFET

➤ Features

V_{DS}	V_{GS}	$R_{DS(ON)}$ Typ.	I_D
-16V	$\pm 12V$	7.8m Ω @-4.5V	-45A
		10.2m Ω @-2.5V	

➤ Description

The SSC8219GN4 is P-Channel enhancement mode MOSFET. Uses trench Technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in DC - DC conversion, power switch and charging circuit.

100% UIS + ΔV_{DS} + R_g Tested!

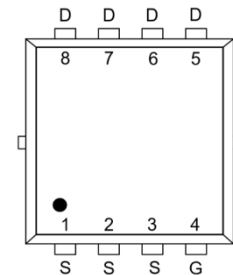
➤ Applications

- Inverter
- DC-DC Converter
- Half and Full Bridge Topology
- Motor Drive Control

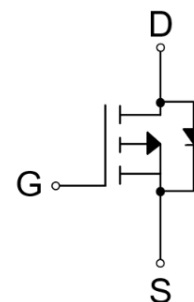
➤ Ordering Information

Device	Package	Shipping
SSC8219GN4	PDFN3.3X3.3-8L	5000/Reel

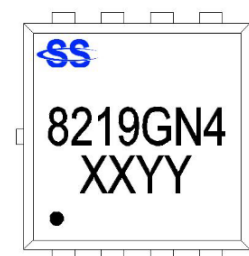
➤ Pin configuration



PDFN3.3x3.3-8L (Top View)



Pin Configuration



Marking

(XXYY: Internal Traceability Code)

**➤ Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)**

Symbol	Parameter		Ratings	Unit
V_{DSS}	Drain-to-Source Voltage		-16	V
V_{GSS}	Gate-to-Source Voltage		± 12	V
I_D	Continuous Drain Current ^b	$T_C = 25^{\circ}\text{C}$	-45	A
		$T_C = 100^{\circ}\text{C}$	-24	A
I_{DM}	Pulsed Drain Current ^b		-178	A
I_{DSM}	Continuous Drain Current ^a	$T_A = 25^{\circ}\text{C}$	-16	A
		$T_A = 70^{\circ}\text{C}$	-11.4	A
P_D	Power Dissipation ^c	$T_C = 25^{\circ}\text{C}$	25	W
		$T_C = 100^{\circ}\text{C}$	10	W
P_{DSM}	Power Dissipation ^a	$T_A = 25^{\circ}\text{C}$	3.2	W
		$T_A = 70^{\circ}\text{C}$	2	W
I_{AS}	Avalanche Current ^b $L = 0.5\text{mH}$		-19	A
E_{AS}	Avalanche Energy ^b $L = 0.5\text{mH}$		90	mJ
T_J	Operation junction temperature		-55 to 150	$^{\circ}\text{C}$
T_{STG}	Storage temperature range		-55 to 150	$^{\circ}\text{C}$

➤ Thermal Resistance Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance ^a	40	$^{\circ}\text{C/W}$
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	3.7	

Note:

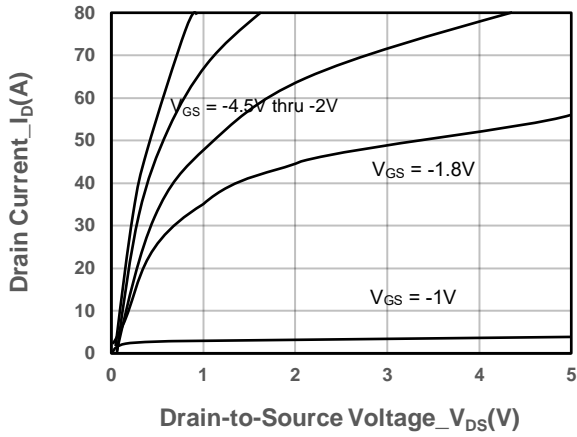
- The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user's specific board design. The current rating is based on the $t \leq 10\text{s}$ thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The power dissipation P_D is based on $T_{J(MAX)}=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- The maximum current rating is package limited.

➤ **Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)**

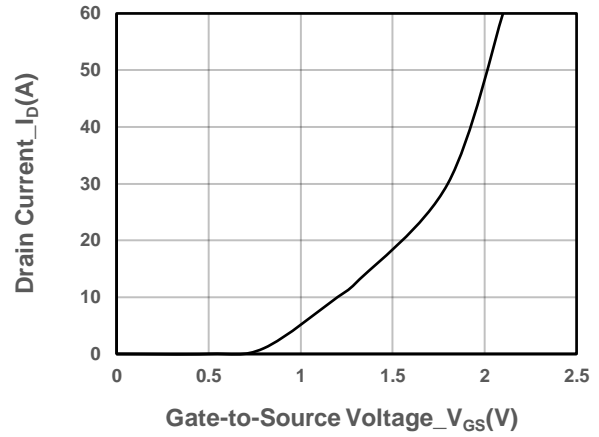
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-16			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-0.4	-0.6	-1	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = -4.5V, I_D = -4.1A$		7.8	10.5	m Ω
		$V_{GS} = -2.5V, I_D = -3A$		10.2	14	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -16V, V_{GS} = 0V$			-1	μA
Gate-Source Leak Current	I_{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0V$			± 100	nA
Forward Voltage	V_{SD}	$V_{GS} = 0V, I_S = -3A$			-1.2	V
Gate Resistance	R_G	$V_{DS} = 0V, f = 1MHz$		2.5		Ω
Input Capacitance	C_{ISS}	$V_{DS} = -10V, V_{GS} = 0V,$ $f = 1MHz$		4568		pF
Output Capacitance	C_{OSS}			549		
Reverse Transfer Capacitance	C_{RSS}			503		
Total Gate Charge	Q_G	$V_{GS} = -4.5V, V_{DS} = -10V,$ $I_D = -15A$		56		nC
Gate to Source Charge	Q_{GS}			8		
Gate to Drain Charge	Q_{GD}			15		
Turn-on Delay Time	$T_{D(ON)}$	$V_{GS} = -10V, V_{DS} = -10V,$ $I_D = -13A, R_G = 27\Omega$		12		ns
Rise Time	T_r			110		
Turn-off Delay Time	$T_{D(OFF)}$			158		
Fall Time	T_f			157		
Diode Recovery Time	T_{rr}	$I_F = -20A, di/dt = -100A/\mu s$		23		ns
Diode Recovery Charge	Q_{rr}	$I_F = -20A, di/dt = -100A/\mu s$		14		nC



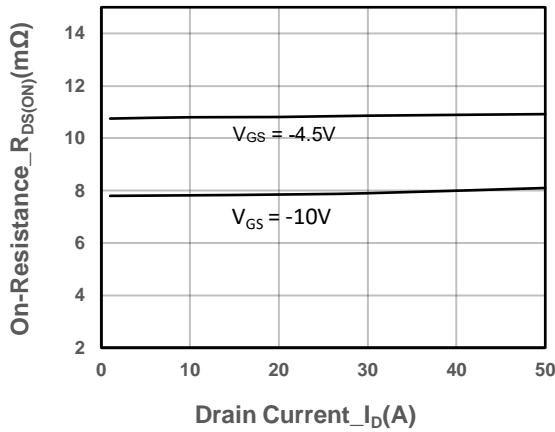
➤ Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)



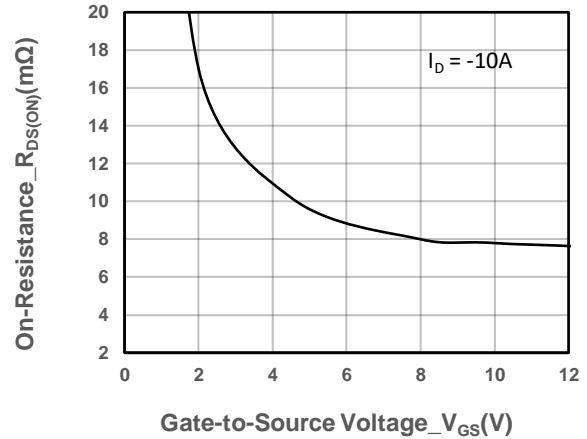
Output Characteristics



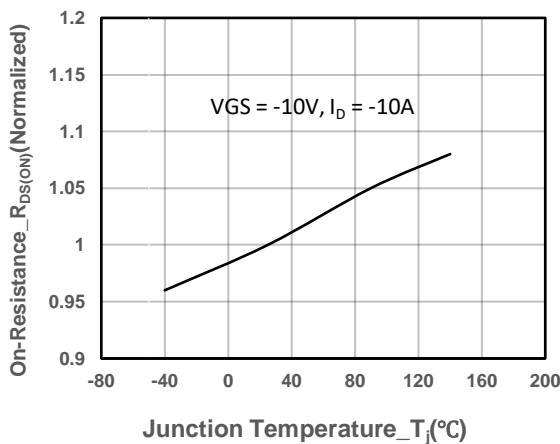
Transfer Characteristics



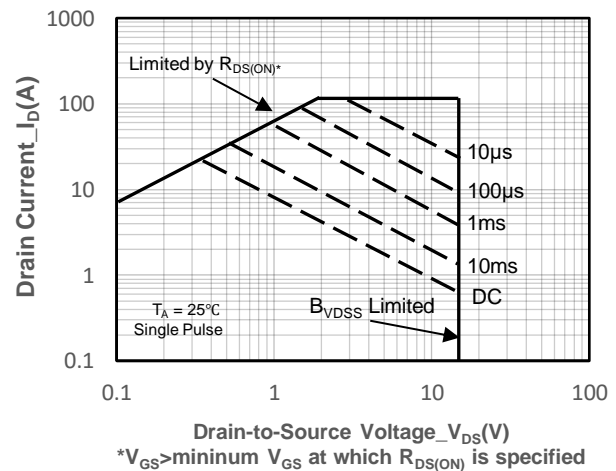
On-Resistance vs. Drain Current and Gate Voltage



On-Resistance vs. Gate-to-Source Voltage

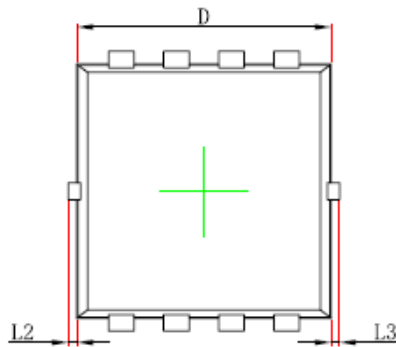


On-Resistance vs. Junction Temperature

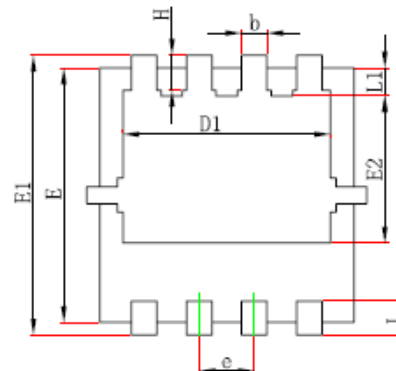


Safe Operating Area vs. Junction-to-Ambient

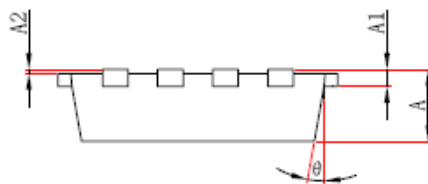
➤ Package Information



Top View
[顶视图]



Bottom View
[背视图]



Side View
[侧视图]

Package: PDNF3.3X3.3-8L

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.650	0.850	0.026	0.033
A1	0.152 REF.		0.006 REF.	
A2	0~0.05		0~0.002	
D	2.900	3.100	0.114	0.122
D1	2.300	2.600	0.091	0.102
E	2.900	3.100	0.114	0.122
E1	3.150	3.450	0.124	0.136
E2	1.535	1.935	0.060	0.076
b	0.200	0.400	0.008	0.016
e	0.550	0.750	0.022	0.030
L	0.300	0.500	0.012	0.020
L1	0.180	0.480	0.007	0.019
L2	0~0.100		0~0.004	
L3	0~0.100		0~0.004	
H	0.315	0.515	0.012	0.020
θ	9°	13°	9°	13°



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